

Representation of Numbers

- Computer represent all numbers, other than integers and some fractions with imprecision.
- Numbers are stored in some approximation which can be represented by a fixed number of bits or bytes.
- There are different types of "representations" or "data types".





• What is the largest integer value that can be expressed in 32 bits?

32-bit Unsigned Integers

Maximum Value

 $2^{32}-1 = 4,294,967,295$

Fixed point Integer Representation Signed Magnitude: Leading bit represents sign and remaining bits represent the corresponding unsigned number. One's Complement: Negative representation is obtained by flipping all the bits of the unsigned number. Two's Complement: Negative representation is obtained by flipping all the bits and adding one.

iked point Integer Representation

- A common method of integer representation is sign and magnitude rep.
- One bit is used for the sign and the remaining bits for the magnitude.
- For example, let 0 denote positive and 1 denote negative
- Clearly there is a restriction to the numbers which can be represented.



Things to note: Things to note: Fixed point numbers are represented as exact. Arithmetic between fixed point numbers is also exact provided the answer is within range. Division is also exact if interpreted as producing an integer and discarding any remainder.

Ones and twos compliment representation

- Can be used for negative numbers.
- The ones compliment form of a negative number is the bitwise NOT applied to it
- Ie. The compliment of its positive counterpart
- The twos compliment form of a negative number is the bitwise NOT applied to it with a 1 added to it.





Floating point Representation

 In floating point representation, numbers are represented by a sign bit s, an integer component e, a positive integer mantissa M.



Floating point Representation

- Most computers use the IEEE representation where the floating point number is normalized.
- The two most common IEEE rep are:
- IEEE Short Real (Single Precision): 32 bits 1 for the sign, 8 for exponent and 23 mantissa
- IEEE Long Real (Double Prec): 64 bits 1 sign bit, 11 for exp and 52 for the mantissa



Represent 27.25 using the IEEE short real rep First Let us convert 27.25 to binary: 11011.01 This is equal to: 0.1101101×2^5 In normal form



Floating Point Numbers

Example 2.

Represent 0.1 using the IEEE short real representation First Let us convert 0.1 to binary: 0.00011001100110011001100110110110...

This is equal to: 0.11001100110011001100110011... x 2⁻³

In normal form













Errors and Uncertainties

- Truncation or approximation errors: these occur from simplifications of mathematics so that the problem may be solved.
- For example replace of an infinite series by a finite series.

• Eg:
$$e^x = \sum_{n=0}^{\infty} \frac{x^n}{n!} \approx \sum_{n=0}^{N} \frac{x^n}{n!} = e^x + \zeta(x, N)$$

Errors and Uncertainties

- Where $\zeta(x, N)$ is the total absolute error.
- The truncation error vanishes as N is taken to infinity.
- For N much larger than x, the error is small.
- If x and N are close then the truncation error will be large.





Errors and Uncertainties

- Propagated error: this is defined as an error in later steps of a program due to an earlier error.
- This error is added the local error(eg. to a roundoff error).

Propagated error is critical as errors may be magnified causing results to be invalid.

• The stability of the program determines how errors are propagated.

Calculating the Error

• A simple way of looking at the error is as the difference between the true value and the actual value.

Ie:

Error (e) = True value – Approximate value















